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# Fax

<b>To:</b>	Examiner Nguyen	<b>From:</b>	Timothy N. Trop
<b>Company:</b>	Patent Office	<b>Date:</b>	March 21, 2003
<b>Fax:</b>	(703) 746-3951	<b>Pages:</b>	4
<b>Your Re:</b>	09/966,481	<b>Our Re:</b>	ITL0552US

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Nathan Y. Moyal

Serial No.: 09/966,481

Filed: September 28, 2001

For: Generating Pulses For  
Resetting Integrated Circuits§  
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Art Unit: 2816

Examiner: Hai L. Nguyen

Atty Docket: ITL.0552US  
P11111Box AF  
Commissioner for Patents  
Washington, D.C. 20231SUPPLEMENTAL RESPONSE TO FINAL REJECTION

Sir:

In response to the office action mailed September 5, 2002, and a telephone call from the Examiner on March 20, 2003, please amend the above-referenced patent application as follows:

IN THE CLAIMS

Please amend claim 11 as follows:

11. (Thrice Amended). An integrated circuit comprising:
- an activation circuit to determine whether a supply voltage reaches a predetermined level, said activation circuit including an inverter coupled to the gate of a load transistor, a second transistor coupled to said load transistor and a third transistor coupled between said load transistor and said first transistor;
- a pulse generator to generate pulses to indicate that a supply voltage is ramping up and to terminate the generation of the pulses after the supply voltage reaches a predetermined level; and

Date of Deposit: March 21, 2003  
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. (703) 746-2951) on the date indicated above.

Cynthia L. Hayden  
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